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Docket No.: 1999P1778

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By: Loren D. Pearson

Date: 6/5/02

Receipt
#9

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Andreas Kux, et al.
Appl. No. : 10/002,925
Filed : November 2, 2001
Title : Method and Device for Securing a Multi-Dimensionally Constructed Chip Stack
and Chip Configuration
Art Unit : 2814.0

REQUEST FOR FILING RECEIPT CORRECTION

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

Undersigned counsel has received the Filing Receipt for the above-identified application.

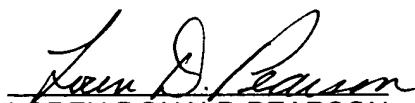
However, the filing date was listed incorrectly and should be as follows:

- November 2, 2001

It is respectfully requested that the Patent Office Records be changed and that a new Filing Receipt be issued, so that the printed patent will show the correct filing date.

Please note that Applicants submitted a Petition under 37 C.F.R 1.10(C) on December 24, 2001.

Respectfully submitted,


LOREN DONALD PEARSON
REG NO. 42,987

Enclosed: Copy of the Filing Receipt
Copy of the express mail label.
Copy of the serial number postcard

/amf

Lerner and Greenberg, P.A.
Post Office Box 2480
Hollywood, FL 33022-2480
Tel: (954) 925-1100
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DET NO. 1999 P.1778..... Mailed November 20, 2001
PLIC. NO. Express Mail EL758653084US
The stamp of the Patent Office hereon may be considered the date on which
papers indicated below were received.

Ap... pgs 26 Rule 53b New Contin Div CIP / Rule 53c Prov. / Rule 53d CPA RCE
 CIP ... pgs Design 3. Dwgs Declaration Mailing Certif.
 Priority Claim Cert. Prior. Doc(s) PCT Cover Sheet WO 01/E7319
 Amend pgs ... Prel. Amend pgs ... Letter
 Response pgs ... 37CFR1.116 Not. of Appeal
 Brief pgs ... Appndx pgs ... I.D.S. + 6 Refs.
 Assoc Pwr of Atty Credit Card \$ 746.00
 Pet. for Ext ... Mo. Pet ... Check \$
 Issue Fee Assignment PTOL 1445
 Cert. Trans. Cert. of Corr. File rec. corr

(Patent Office. Please stamp and return to addressee on reverse side.)

JC580 U.S. PTO
10/002925

11/04/01

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LERNER AND GREENBERG, P.A.
P.O. Box 2480
Hollywood, FL 33022-2480



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COMMISSIONER FOR PATENTS
UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/002,925	11/04/2001	2814	870	1999P1778	3	20	3

CONFIRMATION NO. 5108

LERNER AND GREENBERG, P.A.
Post Office Box 2480
Hollywood, FL 33022-2480

UPDATED FILING RECEIPT


OC000000008152428

Date Mailed: 05/20/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Andreas Kux, Haar, GERMANY;
Michael Smola, Munchen, GERMANY;

Domestic Priority data as claimed by applicant

THIS APPLICATION IS A CON OF PCT/EP00/03834 04/27/2000

Foreign Applications

EUROPEAN PATENT OFFICE (EPO) 99 108 768.5 05/03/1999

If Required, Foreign Filing License Granted 12/26/2001

Projected Publication Date: 08/29/2002

Non-Publication Request: No

Early Publication Request: No

Title

Method and device for securing a multi-dimensionally constructed chip stack and chip configuration

Preliminary Class

RECEIVED MAY 23 2002

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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1	CTRS	5

Total number of pages: 5

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